

1. Description

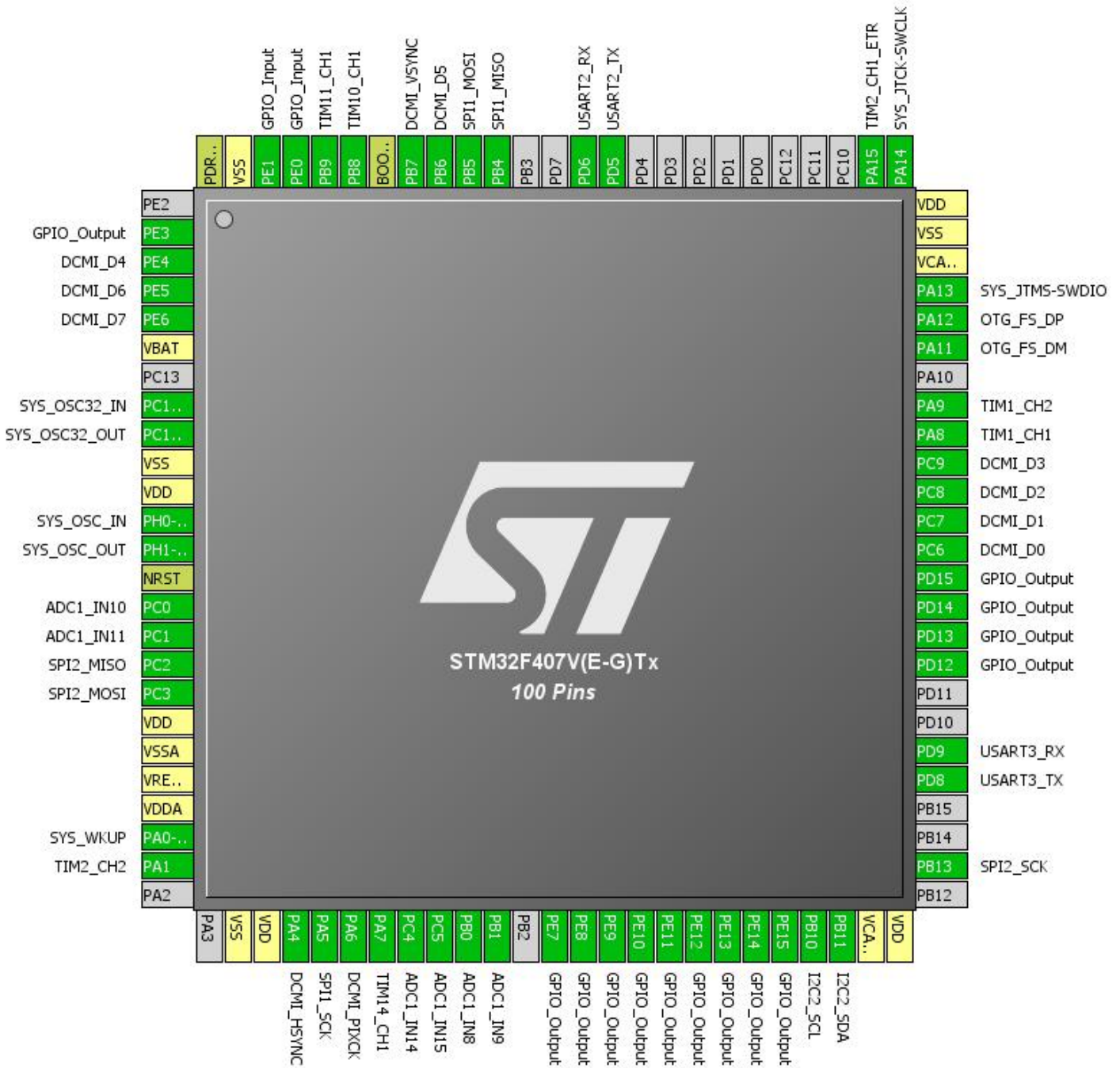
1.1. Project

Project Name	F4 Disc_SDS
Generated with:	MicroXplorer 3.0
Date	05/15/2013

1.2. MCU

MCU family	STM32F4
MCU name	STM32F407V(E-G)Tx
MCU Package	LQFP100
MCU Pin number	100

2. Pinout Configuration



3. Peripherals Configuration

Peripheral	Mode	Remap	Fonction	Pin
ADC1	IN8	0	ADC1_IN8	PB0
	IN9	0	ADC1_IN9	PB1
	IN10	0	ADC1_IN10	PC0
	IN11	0	ADC1_IN11	PC1
	IN14	0	ADC1_IN14	PC4
	IN15	0	ADC1_IN15	PC5
DCMI	Slave-8-bits-External-Synchro	0	DCMI_D0	PC6
			DCMI_D1	PC7
			DCMI_D2	PC8
			DCMI_D3	PC9
			DCMI_D4	PE4
			DCMI_D5	PB6
			DCMI_D6	PE5
			DCMI_D7	PE6
			DCMI_HSYNC	PA4
			DCMI_PIXCK	PA6
DCMI_VSYNC	PB7			
I2C2	I2C	0	I2C2_SCL	PB10
			I2C2_SDA	PB11
OTG_FS	Device-DP-DM	0	OTG_FS_DM	PA11
			OTG_FS_DP	PA12
SPI1	Full-Duplex-Master	0	SPI1_MISO	PB4
			SPI1_MOSI	PB5
			SPI1_SCK	PA5
SPI2	Full-Duplex-Master	0	SPI2_MISO	PC2
			SPI2_MOSI	PC3
			SPI2_SCK	PB13
SYS	Serial-Wire	0	SYS_JTCK-SWCLK	PA14
			SYS_JTMS-SWDIO	PA13
	HSE-External-Oscillator	0	SYS_OSC_IN	PH0-OSC_IN
			SYS_OSC_OUT	PH1-OSC_OUT
	LSE-External-Oscillator	0	SYS_OSC32_IN	PC14-OSC32_IN
			SYS_OSC32_OUT	PC15-OSC32_OUT
SYS-WakeUp	0	SYS_WKUP	PA0-WKUP	
TIM1	Encoder-Interface	0	TIM1_CH1	PA8
			TIM1_CH2	PA9
TIM2	Encoder-Interface	0	TIM2_CH1_ETR	PA15
			TIM2_CH2	PA1
TIM10	PWM-Generation	0	TIM10_CH1	PB8

F4 Disc_SDS Project

Peripheral	Mode	Remap	Fonction	Pin
TIM11	PWM-Generation	0	TIM11_CH1	PB9
TIM14	PWM-Generation	0	TIM14_CH1	PA7
USART2	Asynchronous	0	USART2_RX	PD6
			USART2_TX	PD5
USART3	Asynchronous	0	USART3_RX	PD9
			USART3_TX	PD8

4. Pins Configuration

Pins	Pos	Functions
PE3 *	2	GPIO_Output
PE4	3	DCMI_D4
PE5	4	DCMI_D6
PE6	5	DCMI_D7
PC14-OSC32_IN	8	SYS_OSC32_IN
PC15-OSC32_OUT	9	SYS_OSC32_OUT
PH0-OSC_IN	12	SYS_OSC_IN
PH1-OSC_OUT	13	SYS_OSC_OUT
PC0	15	ADC1_IN10
PC1	16	ADC1_IN11
PC2	17	SPI2_MISO
PC3	18	SPI2_MOSI
PA0-WKUP	23	SYS_WKUP
PA1	24	TIM2_CH2
PA4	29	DCMI_HSYNC
PA5	30	SPI1_SCK
PA6	31	DCMI_PIXCK
PA7	32	TIM14_CH1
PC4	33	ADC1_IN14
PC5	34	ADC1_IN15
PB0	35	ADC1_IN8
PB1	36	ADC1_IN9
PE7 *	38	GPIO_Output
PE8 *	39	GPIO_Output
PE9 *	40	GPIO_Output
PE10 *	41	GPIO_Output
PE11 *	42	GPIO_Output
PE12 *	43	GPIO_Output
PE13 *	44	GPIO_Output
PE14 *	45	GPIO_Output
PE15 *	46	GPIO_Output
PB10	47	I2C2_SCL
PB11	48	I2C2_SDA
PB13	52	SPI2_SCK
PD8	55	USART3_TX
PD9	56	USART3_RX
PD12 *	59	GPIO_Output
PD13 *	60	GPIO_Output
PD14 *	61	GPIO_Output

Pins	Pos	Functions
PD15 *	62	GPIO_Output
PC6	63	DCMI_D0
PC7	64	DCMI_D1
PC8	65	DCMI_D2
PC9	66	DCMI_D3
PA8	67	TIM1_CH1
PA9	68	TIM1_CH2
PA11	70	OTG_FS_DM
PA12	71	OTG_FS_DP
PA13	72	SYS_JTMS-SWDIO
PA14	76	SYS_JTCK-SWCLK
PA15	77	TIM2_CH1_ETR
PD5	86	USART2_TX
PD6	87	USART2_RX
PB4	90	SPI1_MISO
PB5	91	SPI1_MOSI
PB6	92	DCMI_D5
PB7	93	DCMI_VSYNC
PB8	95	TIM10_CH1
PB9	96	TIM11_CH1
PE0 *	97	GPIO_Input
PE1 *	98	GPIO_Input

* The pin is affected with an I/O function